

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hideshi Tsugane et al.  
Title: KEY INPUT CIRCUIT, POWER SUPPLY CONTROL APPARATUS,  
AND POWER SUPPLY CONTROL METHOD  
Docket No.: 32988US1

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

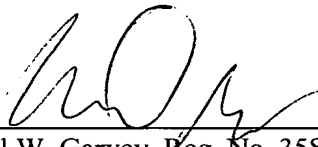
Sir:

In accordance with 37 C.F.R. § 1.98, applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. Per 37 C.F.R. § 1.98(d), copies of the references are not required since each reference listed was cited in application Serial No. 09/669,168, filed on September 25, 2000.

Respectfully submitted,

PEARNE & GORDON LLP

By:

  
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Date: October 17, 2003

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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. <b>32988US1</b>	SERIAL NO. <b>TBD</b>
INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)  Page 1 of 2		APPLICANT: <b>Hideshi Tsugane et al.</b>	
		FILING DATE: <b>Herewith</b>	GROUP ART UNIT: <b>TBD</b>

U.S. PATENT DOCUMENTS							
Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date
	A	4,318,087	3/1982	Wilson			
	B	4,518,951	5/1985	Backes	178	17C	
	C	4,570,154	2/1986	Kinghorn et al.	341	26	
	D	4,667,181	5/1987	Hastreiter	341	26	
	E	4,878,056	10/1989	Isoda et al.	341	25	
	F	4,906,993	3/1990	Freeman et al.	341	22	
	G	5,189,543	2/1993	Lin et al.	359	142	
	H	5,384,721	1/1995	Joto	341	26	
	I	5,402,121	3/1995	Noorbehesht	341	22	
	J	5,554,985	9/1996	Chang	341	22	
	K	5,574,891	11/1996	Hsu et al.	345	168	
	L	5,629,694	5/1997	Simon et al.	341	22	
	M	5,635,958	6/1997	Murai et al.	341	22	
	N	5,702,625	12/1997	Choi	219	506	
	P	5,805,085	9/1998	Hsu et al.			

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		Document No.	Date	Country	Class	Subclass	Translation
	Q	33 23 285 A	1/1985	DE			
	R						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	S	IBM Technical Disclosure Bulletin, Vol. 33, No. 5; October, 1990; Pages 237-238; Interface of a Low-Power Keyboard for Lap-Top Computers.					
	T						

Examiner:	Date Considered
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\*Examiner: Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFORMATION DISCLOSURE CITATION BY APPLICANT <i>(USE SEVERAL SHEETS IF NECESSARY)</i>  <b>Page 2 of 2</b>		APPLICANT: <b>Hideshi Tsugane et al.</b>	
		FILING DATE: <b>Herewith</b>	GROUP ART UNIT: <b>TBD</b>

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Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date
	A	5,861,822	1/1999	Park et al.	341	176	
	B	5,898,425	4/1999	Sekine	345	168	
	C	6,072,472	6/2000	Shiga	341	22	
	D	6,181,262	1/2001	Bennett	341	175	
	E	6,191,709	2/2001	Cho	341	26	
	F						
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FOREIGN PATENT DOCUMENTS							
		Document No.	Date	Country	Class	Subclass	Translation
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OTHER REFERENCES <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>		
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